

**GUJARAT TECHNOLOGICAL UNIVERSITY****BE - SEMESTER-VI (NEW) EXAMINATION – SUMMER 2022****Subject Code:3160921****Date:10/06/2022****Subject Name:HVDC Transmission Systems****Time:10:30 AM TO 01:00 PM****Total Marks: 70****Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.
4. Simple and non-programmable scientific calculators are allowed.

		<b>MARKS</b>
<b>Q.1</b>	(a) Compare of AC and DC Transmission.	<b>03</b>
	(b) What are the limitations of HVDC Transmission lines?	<b>04</b>
	(c) Explain various types of HVDC systems.	<b>07</b>
<b>Q.2</b>	(a) Explain basic principle of three-phase AC–DC conversion.	<b>03</b>
	(b) Describe Commutation Process.	<b>04</b>
	(c) Draw and explain six pulse converter operation.	<b>07</b>
	<b>OR</b>	
	(c) Draw and explain twelve pulse converter operation.	<b>07</b>
<b>Q.3</b>	(a) Explain VSC operating principle.	<b>03</b>
	(b) List out various characteristic of Harmonics.	<b>04</b>
	(c) Describe sinusoidal pulse width modulation with diagram.	<b>07</b>
	<b>OR</b>	
<b>Q.3</b>	(a) How to do selective harmonic elimination?	<b>03</b>
	(b) Explain firing angle control.	<b>04</b>
	(c) What are the various Higher-level Controllers? Explain any one in detail.	<b>07</b>
<b>Q.4</b>	(a) Explain function of smoothing reactors in HVDC system.	<b>03</b>
	(b) Explain principles of DC Link Control in a VSC based HVDC system.	<b>04</b>
	(c) Describe AC voltage regulation using VSC.	<b>07</b>
	<b>OR</b>	
<b>Q.4</b>	(a) Explain function of Ground Electrodes in HVDC system.	<b>03</b>
	(b) Explain principles of DC Link control in a LCC HVDC system.	<b>04</b>
	(c) Explain in detail control of power in MTDC.	<b>07</b>
<b>Q.5</b>	(a) Explain DC line faults in LCC systems.	<b>03</b>
	(b) Describe voltage stability problem in AC/DC systems.	<b>04</b>
	(c) Explain types of multi-terminal HVDC System.	<b>07</b>
	<b>OR</b>	
<b>Q.5</b>	(a) Explain DC line faults in VSC systems.	<b>03</b>
	(b) Explain basic principles of synchronous and asynchronous links.	<b>04</b>
	(c) Explain modular multi-level converters.	<b>07</b>

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