Seat No.:	Enrolment No.

GUJARAT TECHNOLOGICAL UNIVERSITY

Suhi	ect (BE - SEMESTER-VII (NEW) EXAMINATION – WINTER 202 Code:3170914 Date:07-0	
•)1-20 <i>23</i>
•		Name: Advanced Microcontrollers	
		:30 AM TO 01:00 PM Total Ma	rks://
Instru			
	1.	Attempt all questions.	
	2.	Make suitable assumptions wherever necessary.	
		Figures to the right indicate full marks.	
	4.	Simple and non-programmable scientific calculators are allowed.	MARKS
			MAKK
Q.1	(a)	Explain following instructions with examples.	03
		(1) SWI (2) BKPT (3) STC	
	(b)	Write a program to move a floating point value from register R3 to an integer register R4.	04
	(c)	Write a program to transfer block of data from one address to other address.	07
Q.2	(a)	Explain difference between RISC and CISC architectures	03
Q. <u>2</u>	(b)	<u>-</u>	04
	(c)	List out C data types use in programming.	07
	(C)	OR	U7
	(a)		07
Ω_{2}	(c)	<u>•</u>	07
Q.3	(a)	<u>-</u>	03
	(b)	· · · · · · · · · · · · · · · · · · ·	
	(c)	Briefly explain STM32F401 Nucelo board.	07
0.3	(.)	OR	03
Q.3	(a)		03
	(b)		04
	(c)	Explain ARM development tools.	07
Q.4	(a)	Explain following instructions with examples.	03
	` '	(1) MCR (2) ASR (3)MRC	
	(b)		04
	(c)	Explain ARM Processor Exception and Modes.	07
	()	OR	
Q.4	(a)	Explain following instructions with examples.	03
•	()	(1) SWP (2) ADDEQ (3) BNE	
	(b)		04
	(c)	Explain PWM and the Interrupt on STM32F401.	07
	(-)		
Q.5	(a)	Explain CPSR in details.	03
	(b)	Explain ARM addressing modes.	04
	(c)	Explain interfacing of LED and LCD.	07
		OR	
Q.5	(a)	Explain Shift and Rotate Instructions in ARM Cortex.	03
	(b)	Explain Store Instructions in ARM.	04
	(c)	Explain Signed Number Instructions and Operations.	07
