Seat No.:	Enrolment No.

		BE - SEMESTER-VII (NEW) EXAMINATION – SUMMER 2022	
Subj	ect (	Code:3170906 Date:08/0	6/2022
Subj	ect ]	Name:Advanced Power Electronics	
Time	e:02	:30 PM TO 05:00 PM Total Mai	ks: 70
Instru			
		Attempt all questions.  Make suitable assumptions wherever necessary.	
		Figures to the right indicate full marks.	
	4.	Simple and non-programmable scientific calculators are allowed.	
			MARKS
<b>Q.1</b>	(a)		03
	<b>(b)</b>	discontinuous conduction mode.  Discuss static characteristics of SVC.	04
	(b) (c)		0 <del>4</del> 07
		•	
<b>Q.2</b>	(a)	1	03 04
	<b>(b)</b>	(SLR) converter with Parallel loaded resonant (PLR) Converter.	04
	(c)	Discuss discontinuous mode of operation in buck boost converter with	07
		waveforms.	
	(a)	OR Explain zero current switching dc-dc converter.	07
	(c)	Explain zero current switching dc-dc converter.	U/
Q.3	(a)	Explain the concept & need of Multi Level Inverter (MLI)	03
	<b>(b)</b>	What are the advantages of cascaded H bridge multi-level inverter	04
		over other two topologies?	. =
	(c)	Explain the operation of zero voltage switching resonant converter with circuit diagram, waveform and required equation.	07
		OR	
Q.3	(a)	Draw schematic diagram of 12 pulse converter with transformer	03
		connection.	
	<b>(b)</b>	-	04
	(c)	Discuss sine PWM techniques used for multilevel inverter.	07
Q.4	(a)	Classification Of FACTS devices.	03
<b>C</b>	<b>(b)</b>		04
	<b>(c)</b>	· · · · · · · · · · · · · · · · · · ·	07
		multi pulse converter. (a) Y-Z1 (b) $\Delta$ -Z1	
0.4	(0)	OR	02
<b>Q.4</b>	(a) (b)		03 04
	(c)	1	07
	( )	necessary diagram and waveform.	
0.5	( )		0.2
Q.5	(a)		03
	(b) (c)	•	04 07
	(0)	waveforms.	07

## OR

Q.5	(a)	What is the need of Reactive Power Compensation?	03
	<b>(b)</b>	State advantages and limitation of SSSC.	04
	<b>(c)</b>	Explain operating principle of Unified power flow controller (UPFC).	07

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