Subject Code: 3161009

## **GUJARAT TECHNOLOGICAL UNIVERSITY**

## **BE - SEMESTER-VI EXAMINATION - SUMMER 2025**

Date: 30-05-2025

| Subject Name: Embedded Systems Time: 10:30 AM TO 01:00 PM Instructions:  1. Attempt all questions. 2. Make suitable assumptions wherever necessary. 3. Figures to the right indicate full marks. 4. Simple and non-programmable scientific calculators are allowed. |                          |  |                      |
|---|--------------------------|--|----------------------|
| Q.1   | (a)<br>(b)<br>(c)        | Write down skills Required for an Embedded System Designer. What is and Embedded system? What are its characteristics? Explain the working of the Secure Digital Input Output (SDIO) protocol.   | 03<br>04<br>07       |
| Q.2   | (a)<br>(b)<br>(c)        | What is watch dog Timer? Explain its function in brief. How does Brown Out Reset work in microcontroller? Explain the working of Spy-Bi-Wire serial communication protocol.  OR  | 03<br>04<br>07       |
| Q.3   | (c)<br>(a)<br>(b)<br>(c) | List down wireless communication protocol and explain any one in detail.  Describe Earliest Deadline First Mechanism with example.  Explain concept of interrupt service routine.  What is context switching? Explain context switching on interrupts.                         | 07<br>03<br>04<br>07 |
| Q.3   | (a)<br>(b)<br>(c)        | OR  Describe MSP430 USCI module and its modes.  What do you mean by Mutex? Also explain P and V semaphore with appropriate example.  Describe the main problem with semaphores and its limitation.   | 03<br>04<br>07       |
| Q.4   | (a)<br>(b)<br>(c)        | Describe the four sources of clock in MSP430 processor.  MSP430 is having an orthogonal CPU architecture supported with RISC features. – Justify the statement.  What is inter process communication (IPC)? List down and explain methods of IPC in brief.                     | 03<br>04<br>07       |
| Q.4   | (a)<br>(b)<br>(c)        | OR  Explain the special features associated with GPIO port pins in MSP430 other than simple digital input output port pin characteristics.  Sketch diagram to interface DMA with microprocessor or microcontroller Give difference between Multiprocessing and Multithreading. | 03<br>04<br>07       |
| Q.5   | (a)<br>(b)<br>(c)        | List down features of an operating system.  What is meant by benchmarking Real time operating system (RTOS)? Why it is required?  Draw interfacing diagram to interface 8 LEDs with MSP430. Turn-ON LEDs in ring counter fashion.  OR  | 03<br>04<br>07       |
| Q.5   | (a)<br>(b)<br>(c)        | Describe Round Robin with Interrupts.  Explain Rate-monotonic scheduling.  Differentiate between Clock-driven and Event-driven Scheduling.   | 03<br>04<br>07       |